



PATENT APPLICATION  
Docket No: 11675.165.2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yin, et al.

Serial No.: 09/651,386

Filed: August 29, 2000

For: METHOD OF MAKING AN ELECTRICAL DEVICE  
INCLUDING AN INTERCONNECT STRUCTURE

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. § 1.97**

The Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please find, pursuant to 37 C.F.R. § 1.98(a)(1), the enclosed Form PTO-1449 which contains a list of all patents, publications, or other items that have come to the attention of one or more of the individuals designated in 37 C.F.R. § 1.56(c). While no representation is made that any of these references may be "prior art" within the meaning of that term under 35 U.S.C. §§ 102 or 103, the enclosed list of references is disclosed so as to fully comply with the duty of disclosure set forth in 37 C.F.R. § 1.56.

Moreover, while no representation is made that a specific search of office files or patent office records has been conducted or that no better art exists, the undersigned attorney of record believes that the enclosed art is the closest to the claimed invention (taken in its entirety) of which the undersigned is presently aware, and no art which is closer to the claimed invention (taken in its entirety) has been knowingly withheld.

03/04/2002 JADDO1 00000022 09651384

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In accordance with 37 C.F.R. § 1.98(c), all English translations within the possession, custody, control or availability of anyone designated in 37 C.F.R. § 1.56(c) of each non-English reference, if any, are also enclosed.

Since all listed references are either in the English language or are accompanied by a translation into English, no concise explanation of relevance is required under 37 C.F.R. § 1.98(a)(3).

<u>U.S. Patent No.</u>	<u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub Class</u>
5,861,675	1/1999	Sasaki, et al.	257	764

Please credit any over payment or charge any additional fees to Deposit Account No. 23-3178 of the undersigned.

Dated this 25<sup>th</sup> day of February, 2002.

Respectfully submitted,



Jesús Juanós i Timoneda, Ph. D.  
Attorney for Applicant  
Registration No. 43,332

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2814

CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)		Docket No. 11675.165.2	
Applicant(s): Yin, et al.			
Serial No. 09/651,386	Filing Date FEB 25 2002 August 29, 2000	Examiner Tuan Quach	Group Art Unit 2814
Invention: METHOD OF MAKING AN ELECTRICAL DEVICE INCLUDING AN INTERCONNECT STRUCTURE			
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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
11675.165.2

In Re Application Of: Yin, et al.

FEB 25 2002

Serial No.  
09/651,386

Filing Date  
August 29, 2000

Examiner  
Tuan Quach

Group Art Unit  
2814

Title: METHOD OF MAKING AN ELECTRICAL DEVICE INCLUDING AN INTERCONNECT STRUCTURE

Address to:

Assistant Commissioner for Patents  
Washington, D.C. 20231

**37 CFR 1.97(b)**

- The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

- The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

the statement specified in 37 CFR 1.97(e);

OR

the fee set forth in 37 CFR 1.17(p).

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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
11675.165.2

In Re Application: *Yin, et al.*

*FEB 25 2002*

Serial No.  
09/651,386

Filing Date  
August 29, 2000

Examiner  
Tuan Quach

Group Art Unit  
2814

METHOD OF MAKING AN ELECTRICAL DEVICE INCLUDING AN INTERCONNECT

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*Jesus Juanos i Timoneda, Ph. D.*  
Attorney for Applicant  
Reg. No. 43,332



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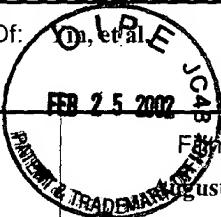
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**TRANSMITTAL LETTER**  
**(General - Patent Pending)**

Docket No.  
**11675.165.2**

In Re Application Of:



Serial No.  
**09/651,386**

Filing Date  
**August 29, 2000**

Examiner  
**Tuan Quach**

Group Art Unit  
**2814**

Title: **METHOD OF MAKING AN ELECTRICAL DEVICE INCLUDING AN INTERCONNECT STRUCTURE**

**TO THE ASSISTANT COMMISSIONER FOR PATENTS:**

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Transmittal Letter (1 pg. in dup); Transmittal of IDS (2 pgs.);  
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Dated: **February 25, 2002**

**Jesus Juanos i Timoneda, Ph. D.**  
 Attorney for Applicant  
 Reg. No. 43,332



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